HD74AC00

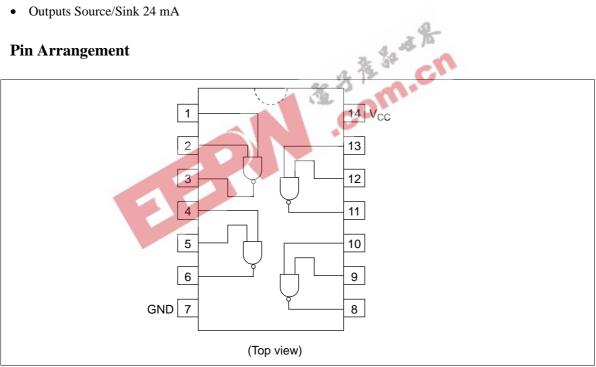
Quad 2-Input NAND Gate

HITACHI

Feature

• Outputs Source/Sink 24 mA

Pin Arrangement



DC Characteristics (unless otherwise specified)

ltem	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I _{cc}	40	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5$ V, Ta = Worst case
Maximum quiescent supply current	I _{cc}	4.0	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, Ta = 25°C



HD74AC00

AC Characteristics

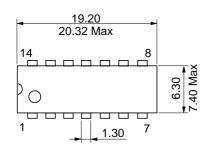
			Ta = +25°C C _∟ = 50 pF			Ta = −40°C to +85°C C _L = 50 pF		
ltem	Symbol	V _{cc} (V)* ¹	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	7.0	9.5	1.0	10.0	ns
		5.0	1.0	6.0	8.0	1.0	8.5	
Propagation delay	t _{PHL}	3.3	1.0	5.5	8.0	1.0	8.5	ns
		5.0	1.0	4.5	6.5	1.0	7.5	

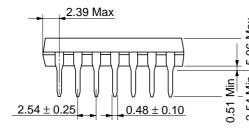
Note: 1. Voltage Range 3.3 is 3.3 V \pm 0.3 V Voltage Range 5.0 is 5.0 V \pm 0.5 V

Capacitance

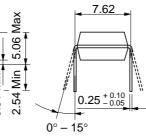
Capacitance				R	
Item	Symbol	Тур	Unit	Condition	
Input capacitance	C _{IN}	4.5	pF	$V_{cc} = 5.5 V$	
Power dissipation capacitance	C _{PD}	30.0	pF	$V_{cc} = 5.0 V$	









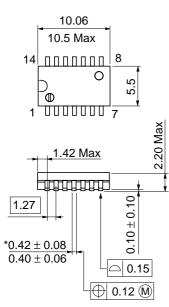


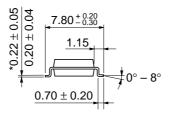
Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

Unit: mm







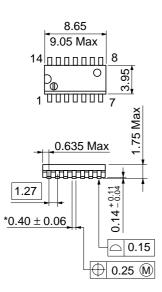


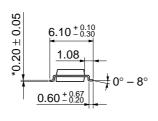
Hitachi Code	FP-14DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.23 g

*Dimension including the plating thickness Base material dimension



Unit: mm

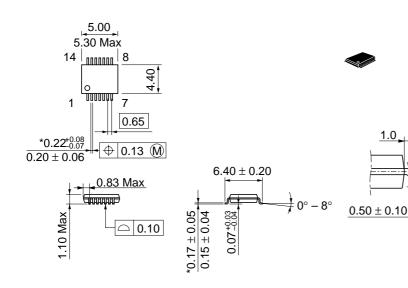




Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g



Unit: mm



Hitachi Code	TTP-14D
JEDEC	
EIAJ	
Weight (reference value)	0.05 g

1.0

*Dimension including the plating thickness Base material dimension

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